

FIG. 1

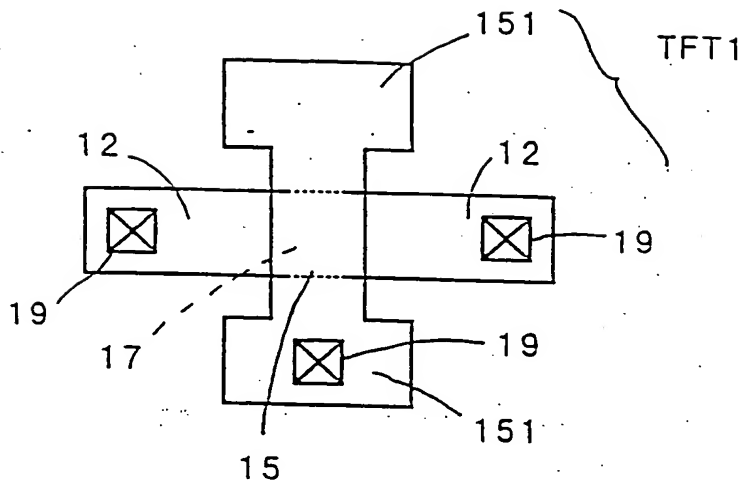


FIG. 2

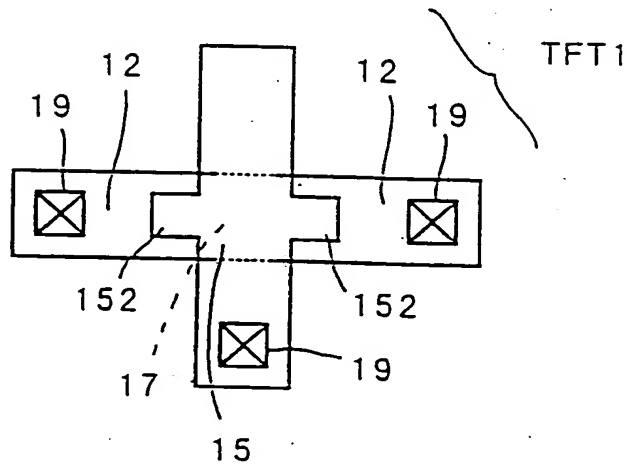


FIG. 3

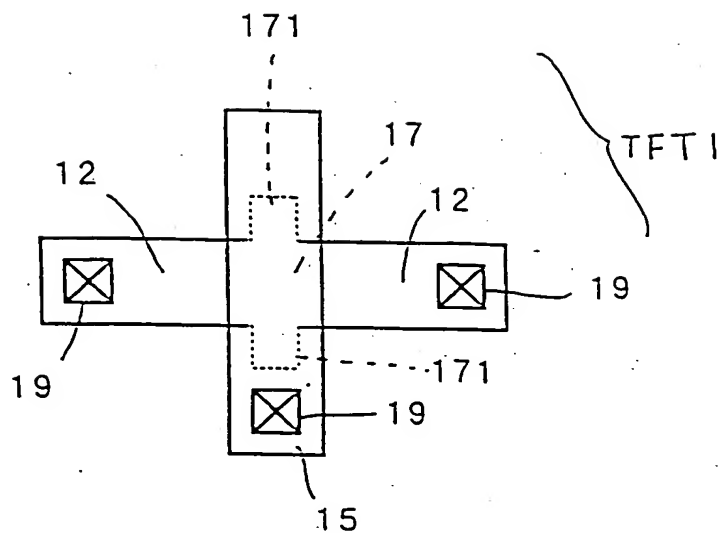


FIG. 4

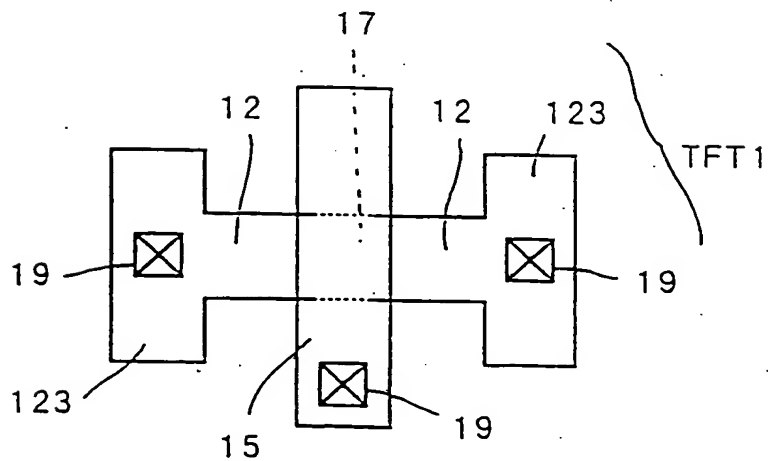


FIG. 5

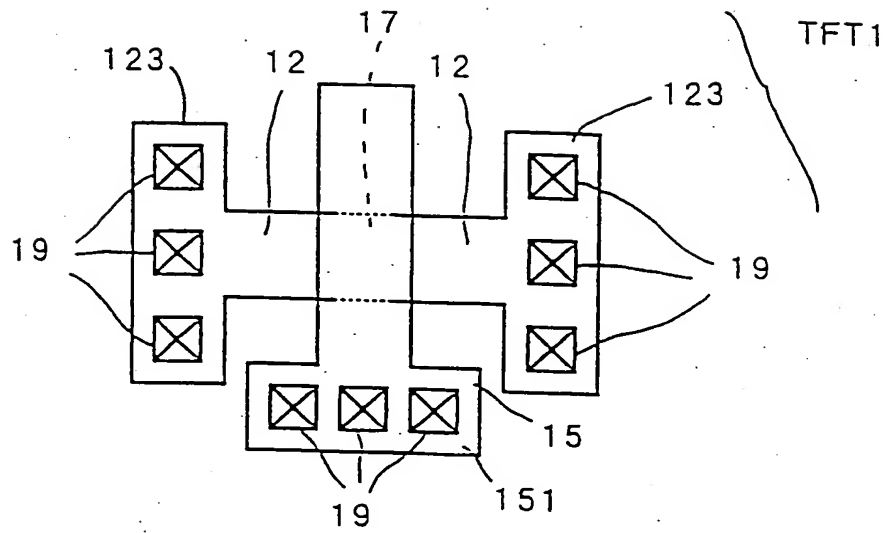


FIG. 6

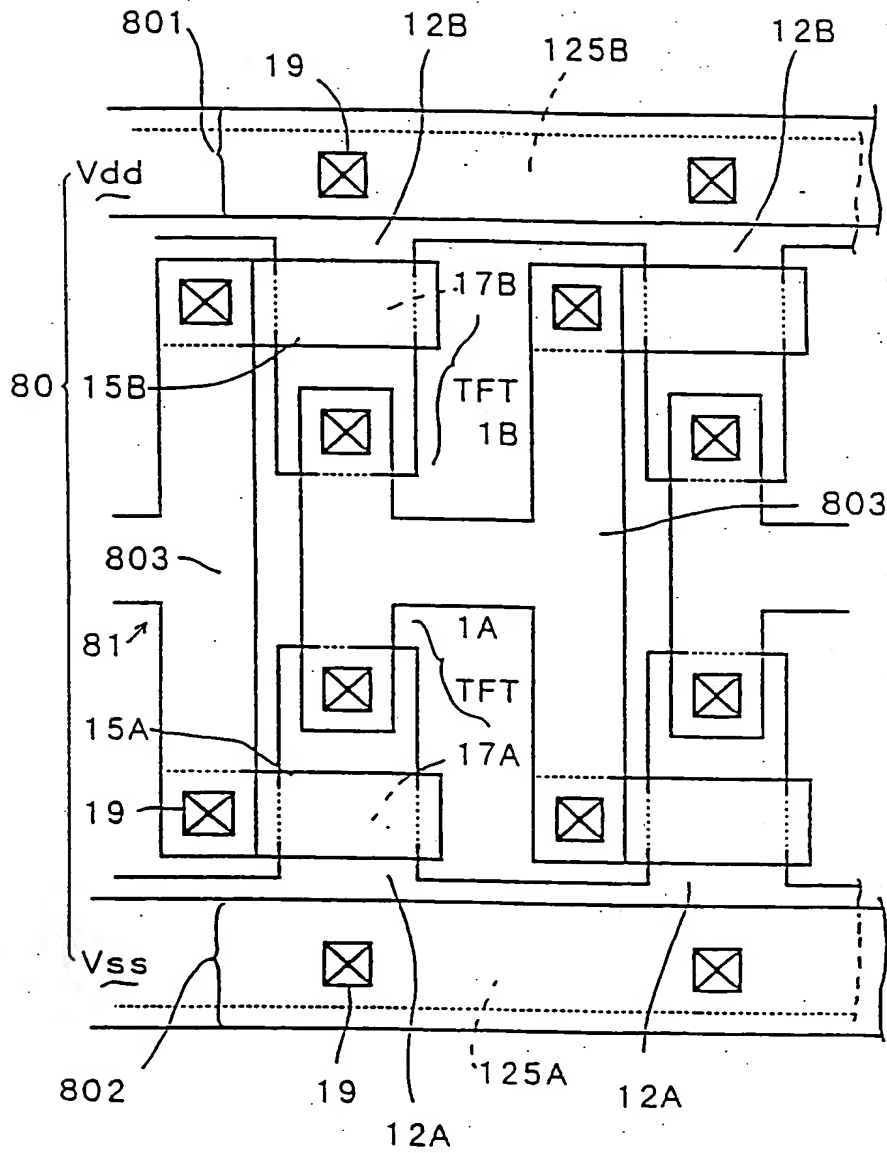
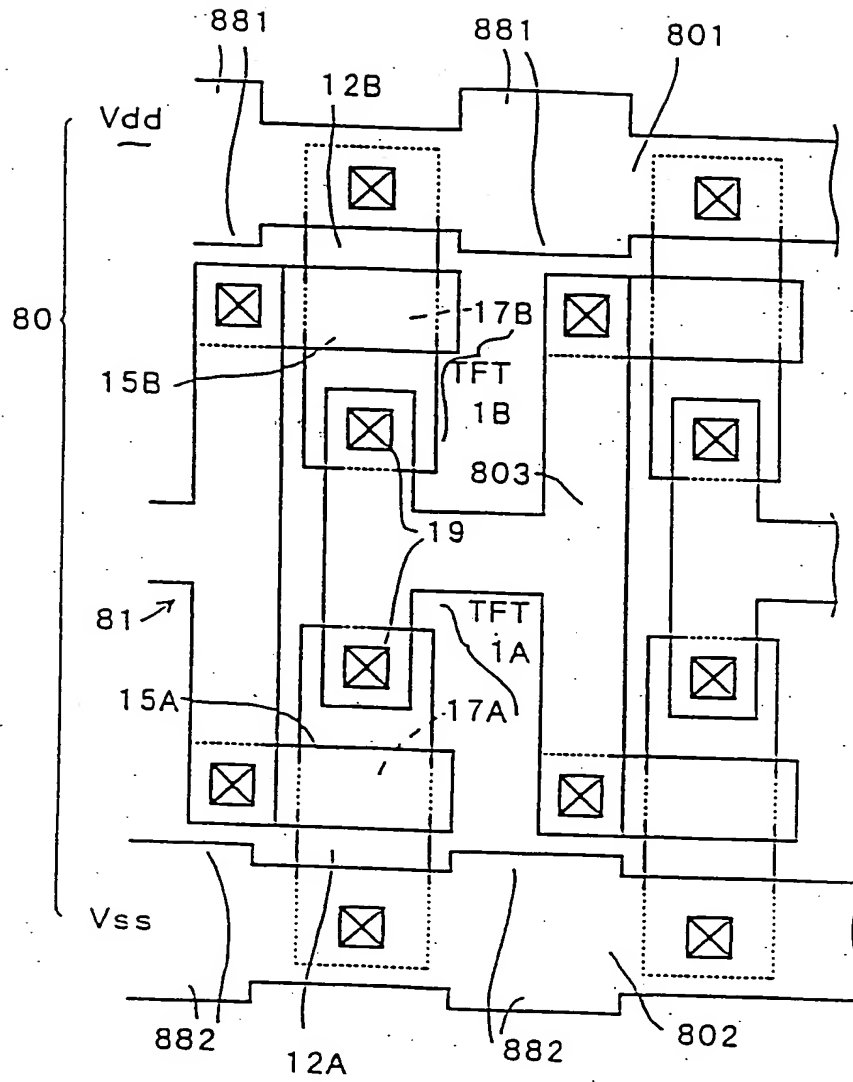


FIG. 7

(A)



(B)

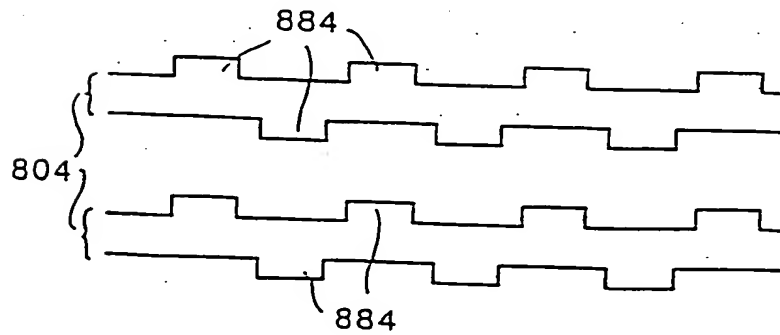
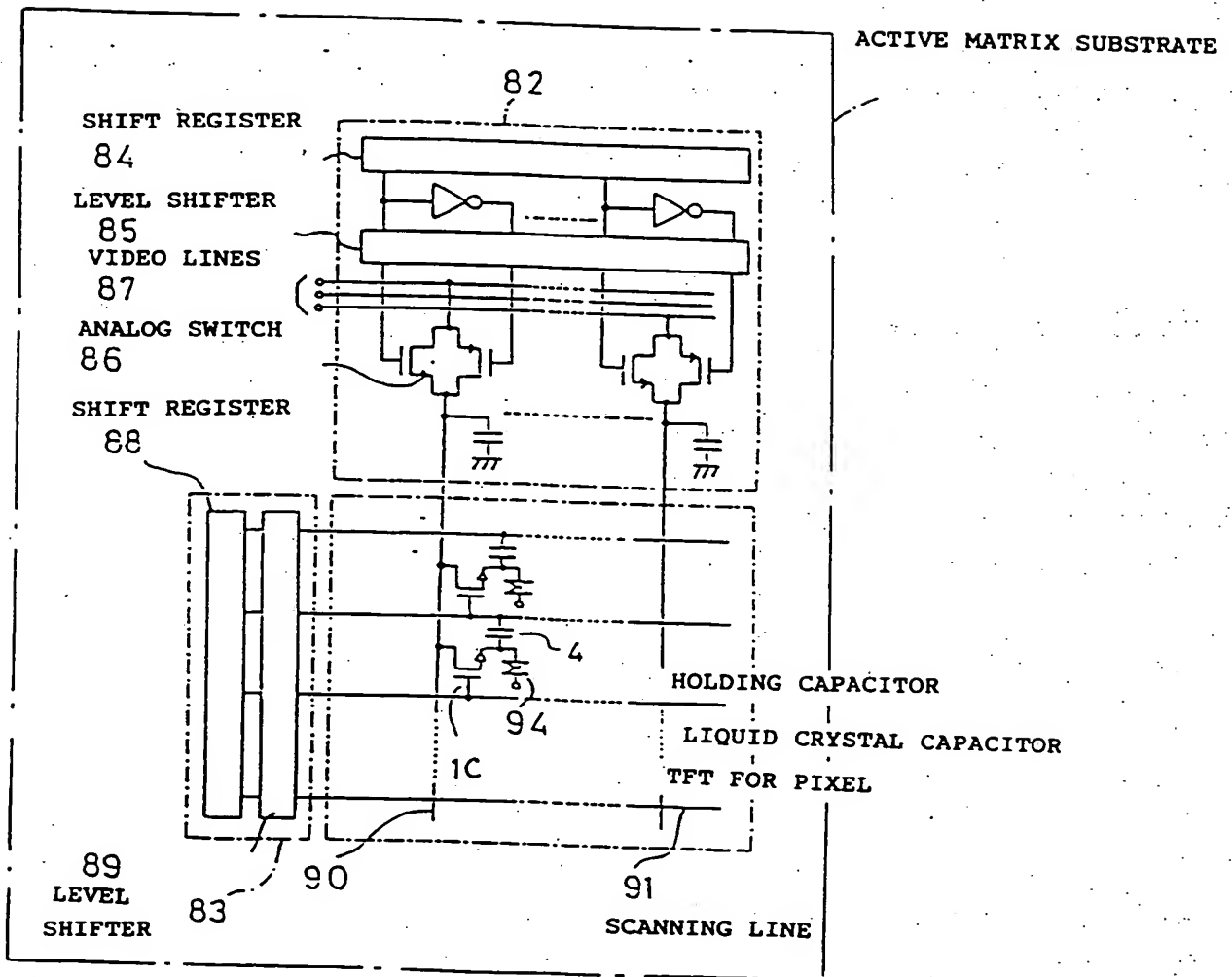
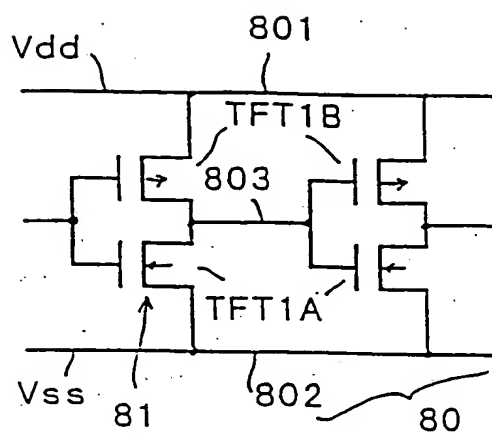


FIG. 8



(A)



(B)

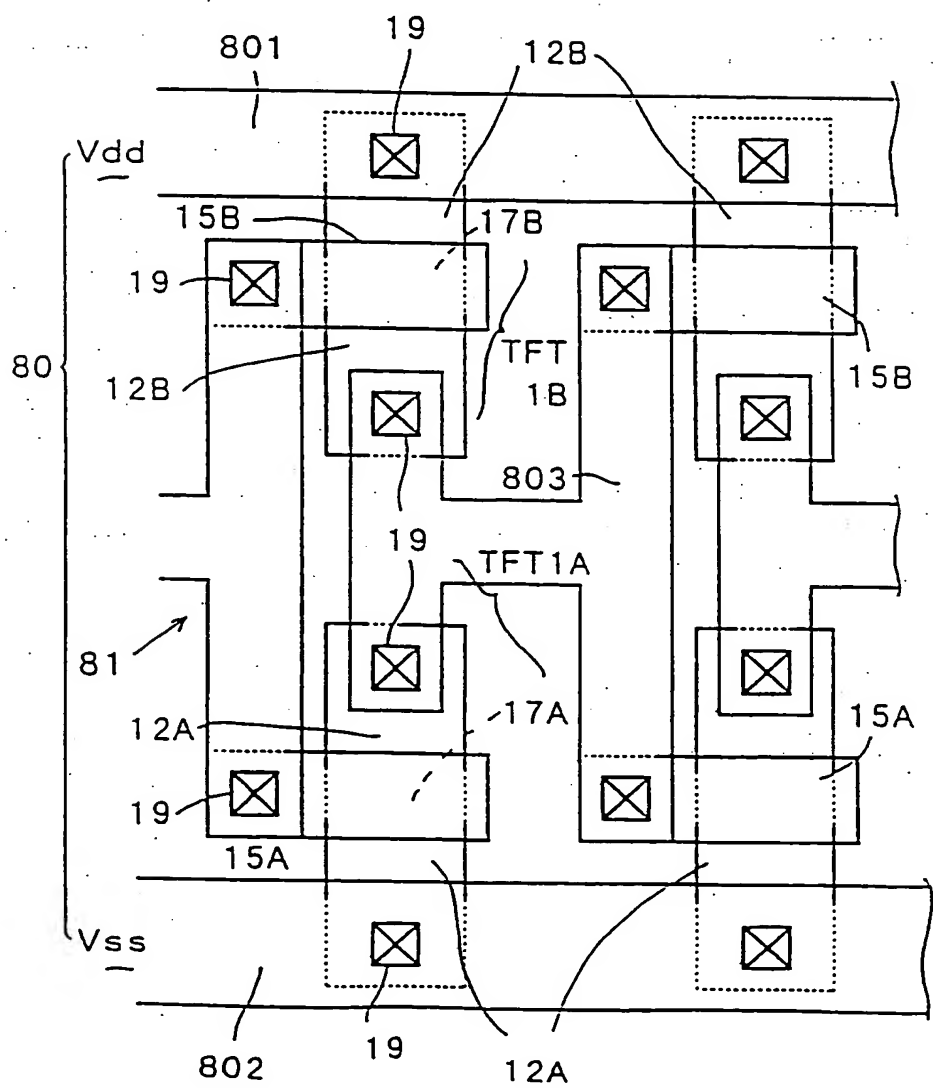


FIG. 10

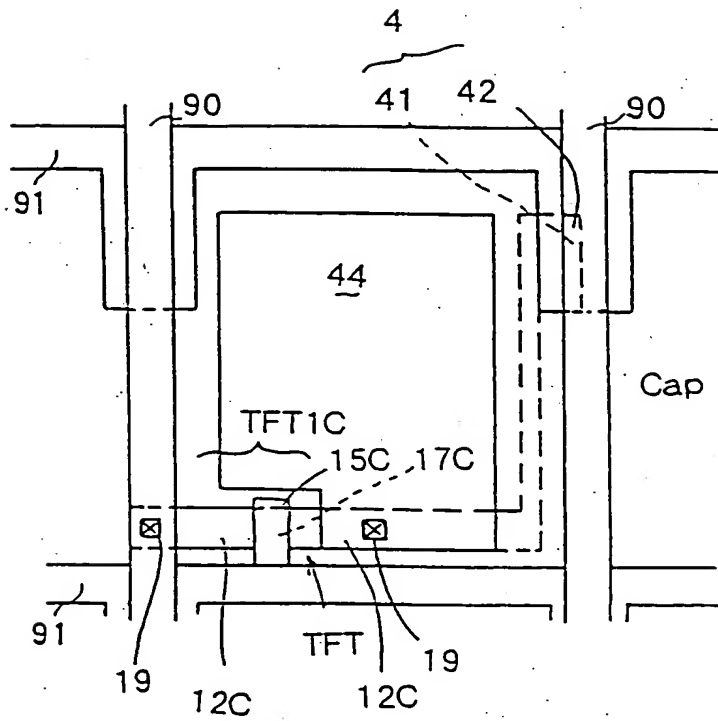
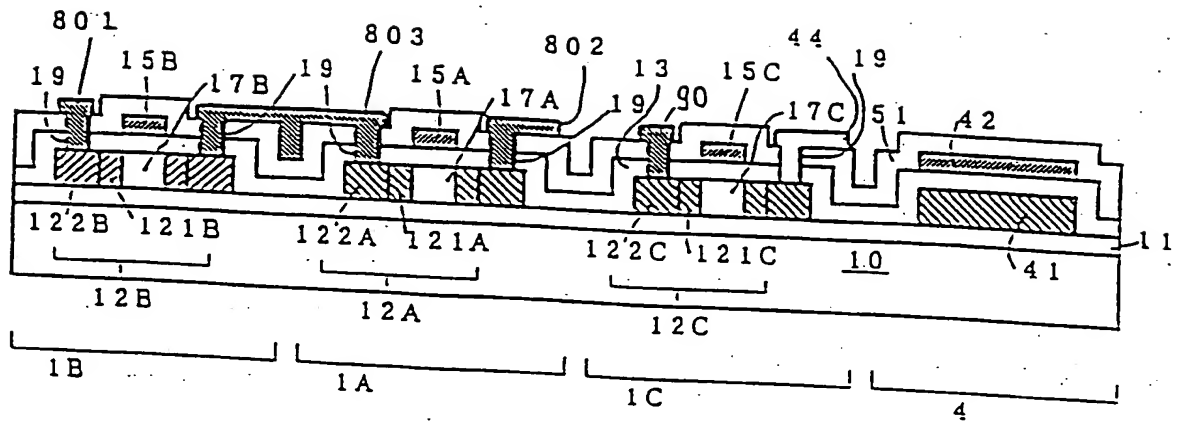


FIG. 11



P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

TFT FOR PIXEL

HOLDING CAPACITOR

FIG. 12

LASER ANNEALING

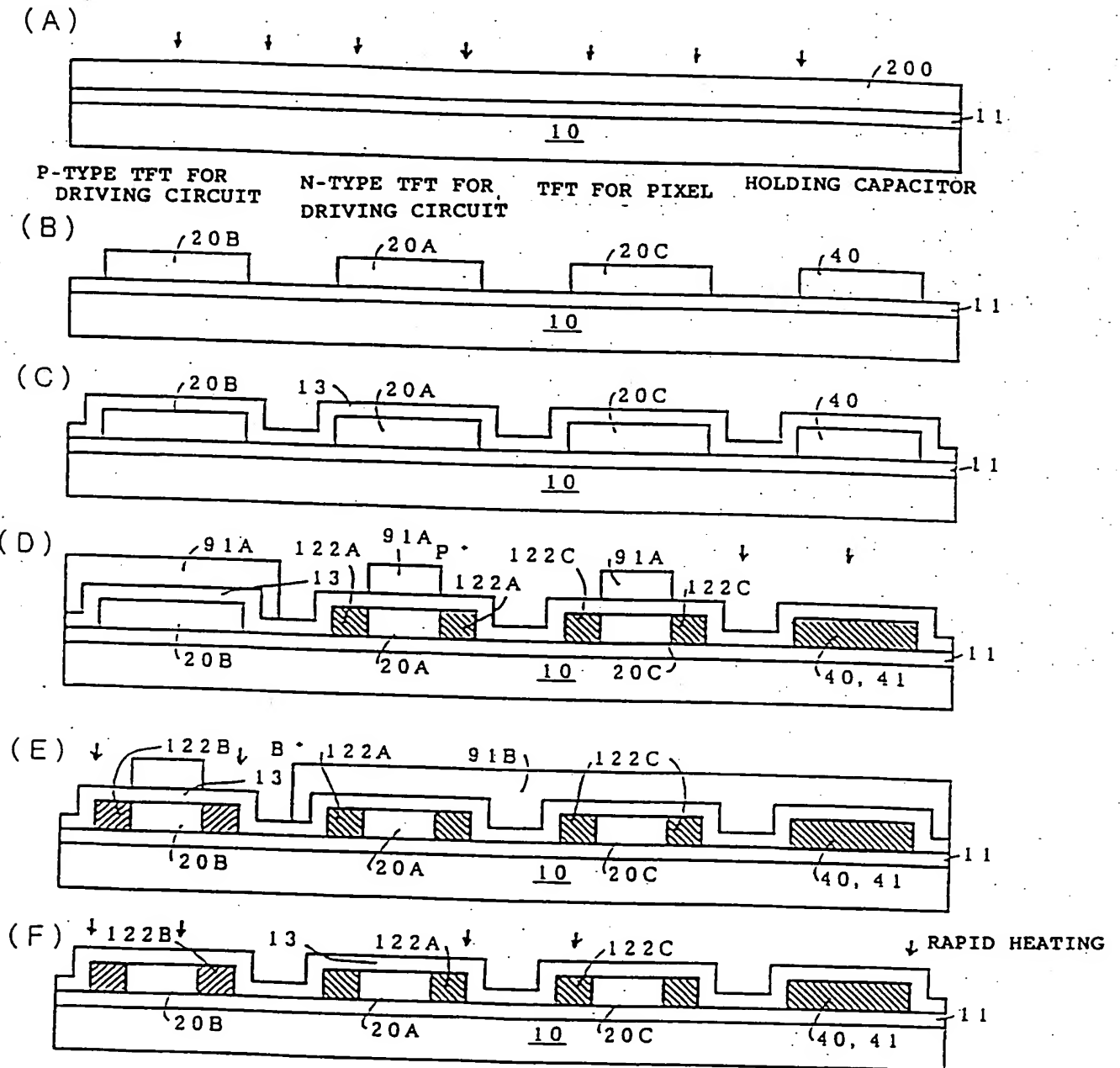
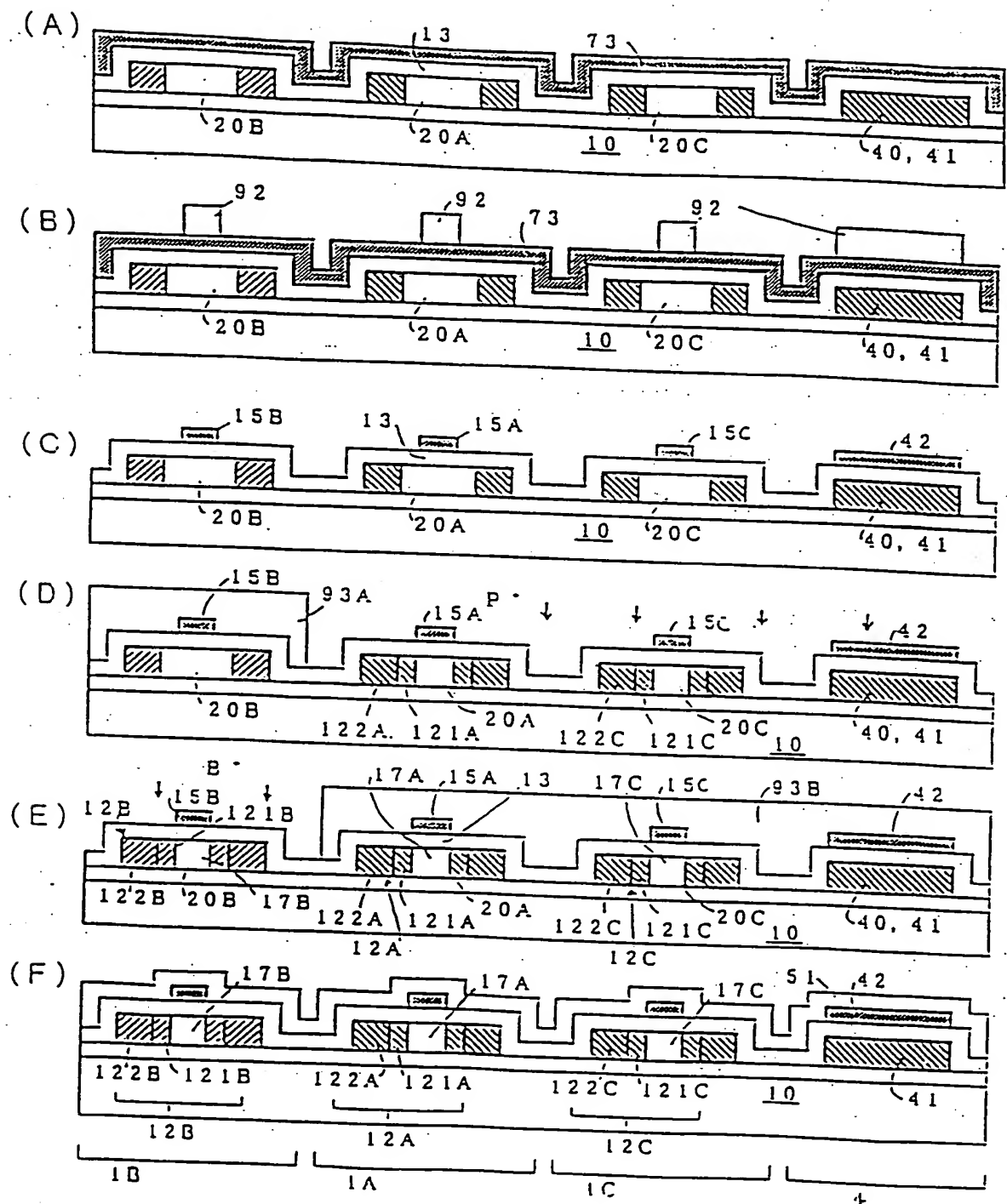


FIG. 13

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P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

TFT FOR PIXEL

HOLDING CAPACITOR

FIG. 14

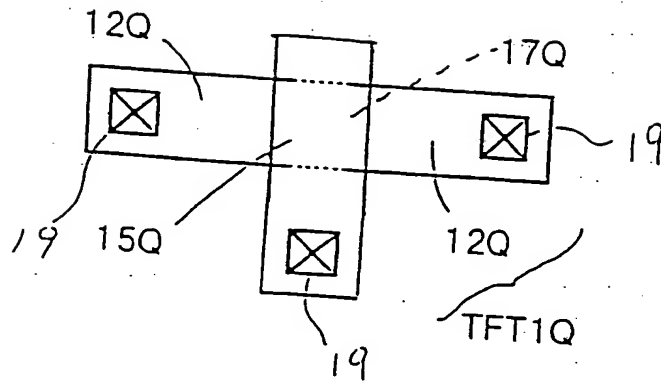


FIG. 15

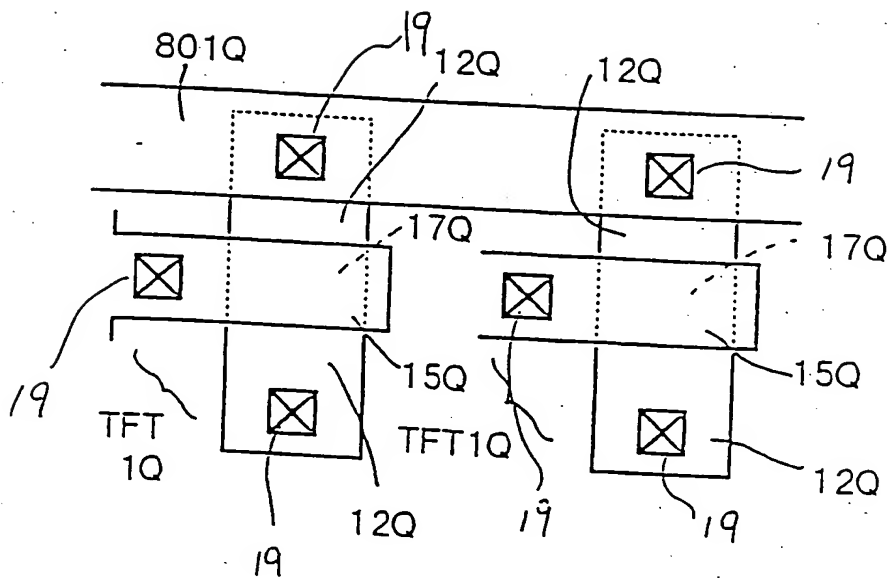


FIG. 16

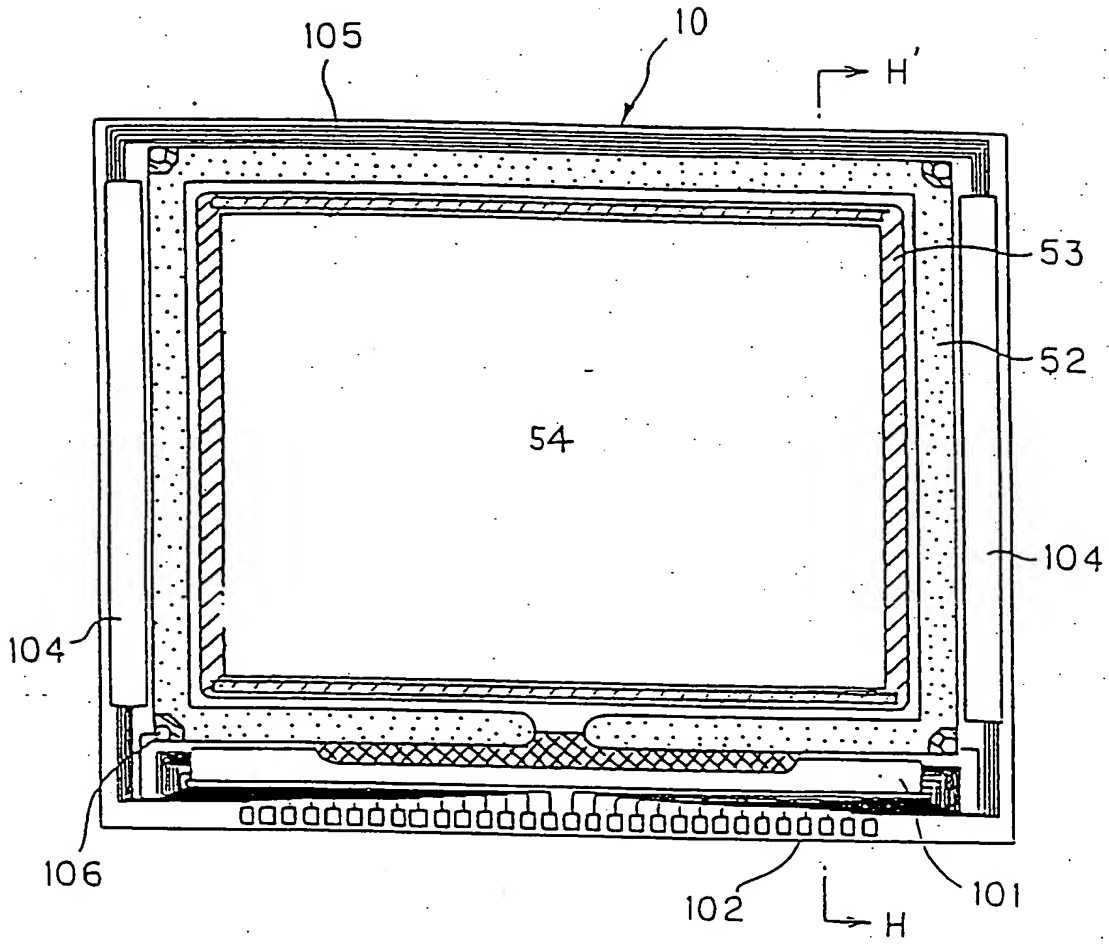


FIG. 17

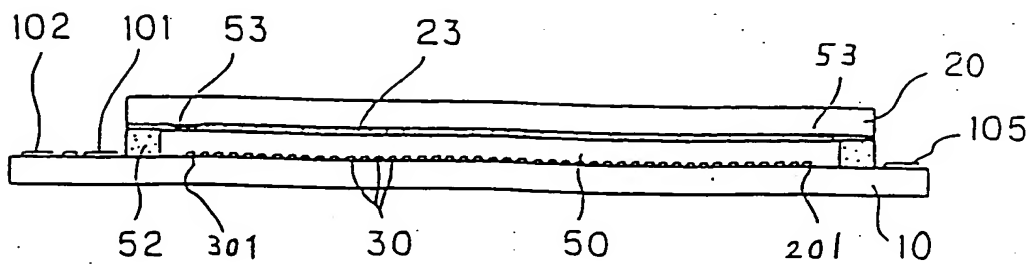


FIG. 18

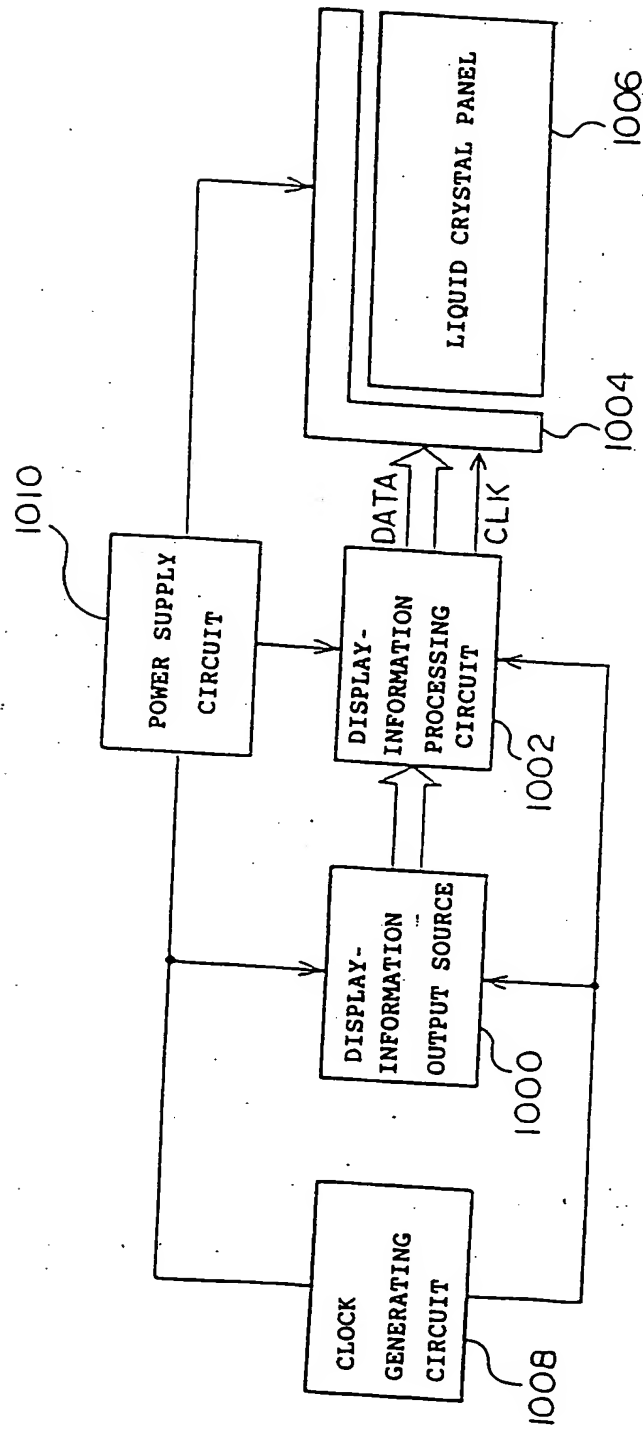


FIG. 19

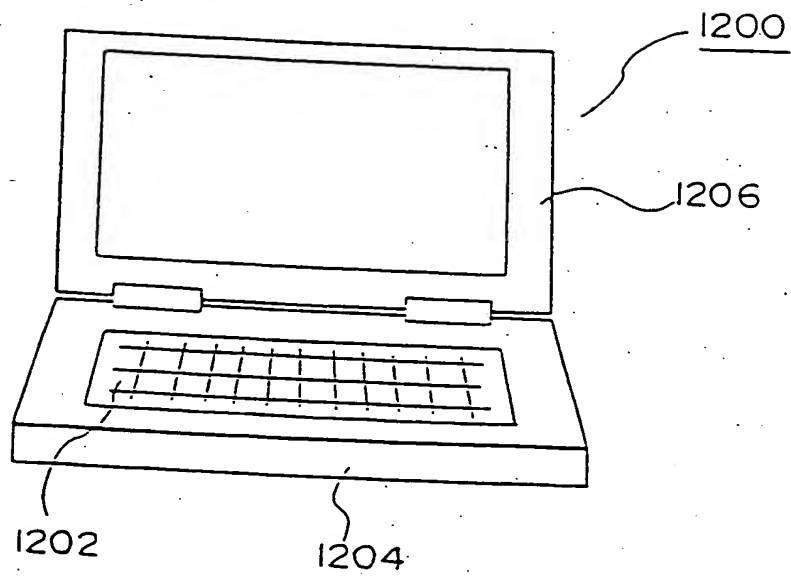


FIG. 20

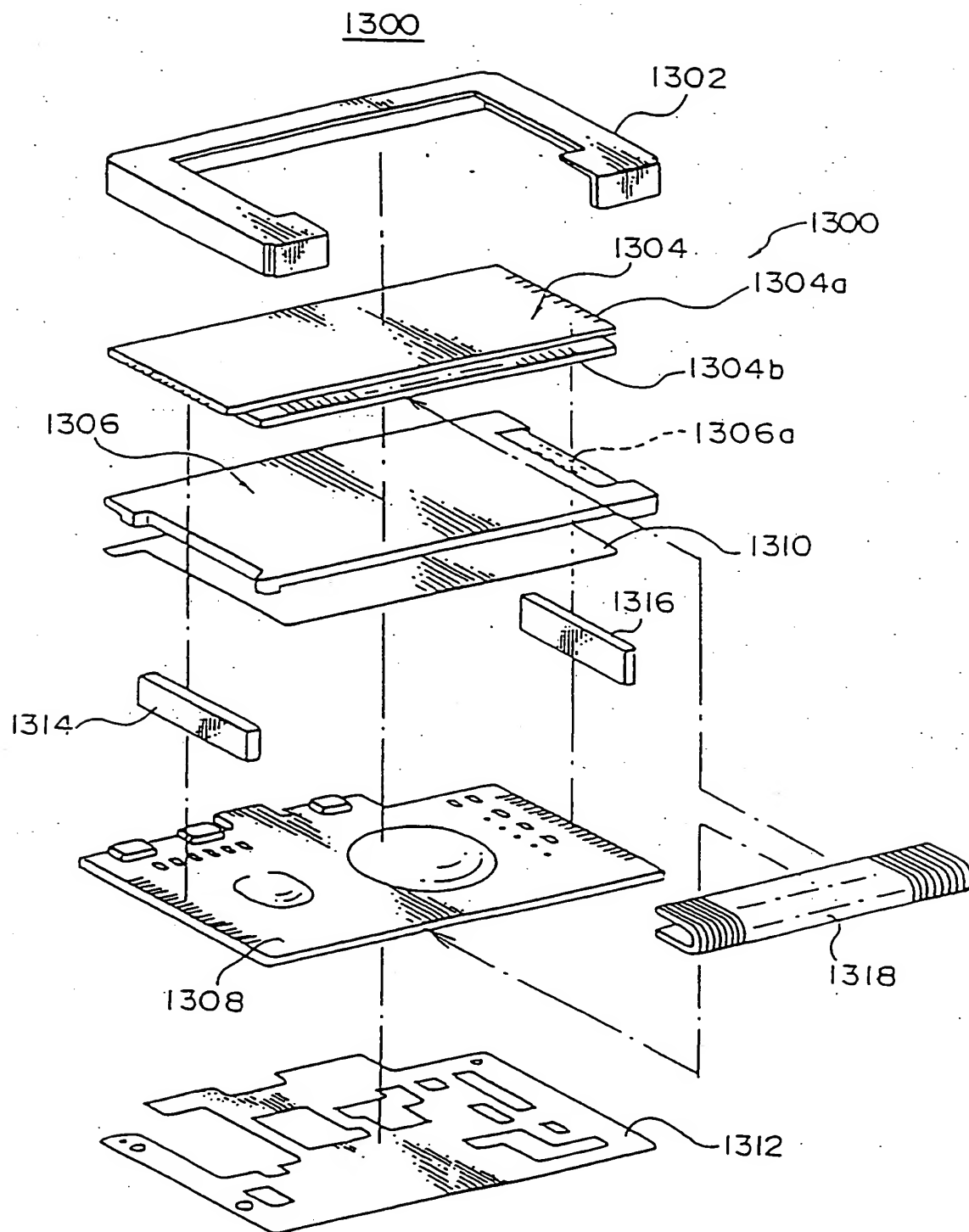


FIG. 21

